FIG. 1

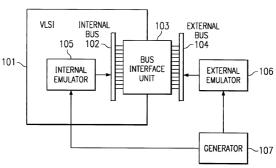
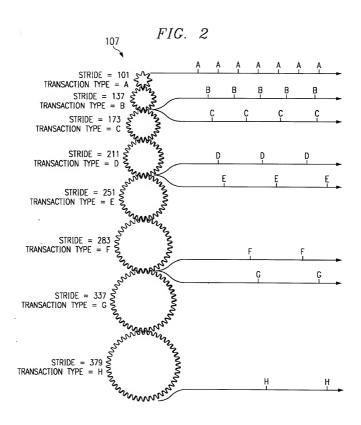


FIG. 3

TIME $201 \sim_{101} - \frac{A}{1}$ $202 \sim_{137} - \frac{A}{1}$ $203 \sim_{173} - \frac{A}{1}$ $201' \sim_{202} - \frac{A}{1}$ $204 \sim_{255} - 256 \sim_{205}$ $202' \sim_{274} - \frac{B}{1}$



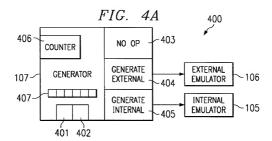
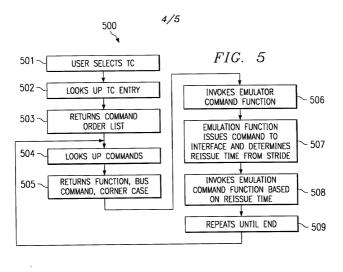


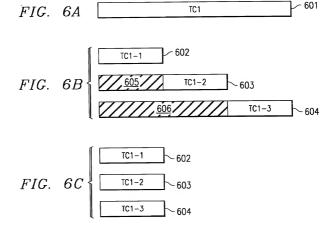
FIG. 4B

	TEST CASE TABLE								
401~	STRIDE	101	137	173	211	٠	•	٠	
	TC1	R	W	T	0	•	•	•	
	TC2	Q	R	W	T		•		
	•		•	•	•				
	•		•	•	•				
	•	٠.	•	•	•				

FIG. 4C

	COMMAND TABLE								
	COMMAND	FUNCTION	BUS COMMAND	CORNER CASE					
402~	0	NO OP							
	Q	GEN_EXT_COMMAND	READ						
	R	GEN_EXT_COMMAND	READ	DIRTY					
	W	GEN_INT_COMMAND	WRITE						
	T	GEN_INT_COMMAND	FLUSH						
	•	•	•						
		:		•					
	_ ·	•	•	•					





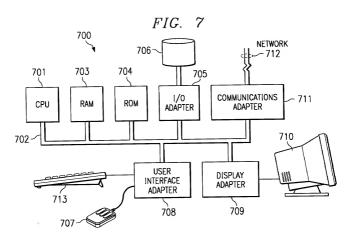


FIG. 8
(PRIOR ART)

INTERNAL
BUS
803
801
VLSI
BUS
INTERFACE
UNIT
804
EXTERNAL
BUS

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